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JAPANESE PATENT OFFICE

## PATENT ABSTRACTS OF JAPAN

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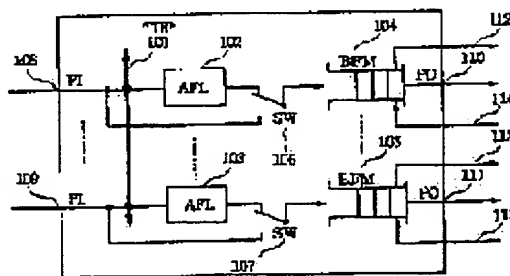
NAGANO HIROSHI

(54) PACKET SWITCH

(57)Abstract:

PURPOSE: To easily realize the extension of a buffer memory arranged for each output port in the packet switch.

CONSTITUTION: A TDB 101 applies time division multiplex processing to a packet inputted from PI108-109. AFL 102-103 check address information of a packet on the TDB 101 and passes only the packet having a relevant address. SW 106-107 select a path for the packet from the AFL 102-103 or a path for a packet of the PI108-109 and the packet is stored in BFM 104-105. Overflow flags 112-113 of the BFM 104-105 are used to inform the overflow to devices at the outside of the switches and output stop command signals 114-115 from the devices at the outside of the switches stop packet output from the BFM 104-105.



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